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2 High Temperature Reverse Bias
3 High Temperature Bias
4 Intermittent Operational Life

Performed on surface mount devices (SMDs) prior to TC, AC, HT, and HAST stresses only.
Tjmax, 80% VR (Zener 100% Vz, TVS 100% Vrwm)
Tjmax, 100% Vgs
Tj 100%, 2min ON/2min OFF

JESD22A-113
JESD22-A108
JESD22-A-108
MIL-STD 883C Method 1037

1. Temperature Cycling: -40 ~ 60
5cycles
2. Bake: 125°C, 24H
3. Moisture: 85±2% RH, 85±5% / 168h
4. Bias: 260V, 3cycles
1000Hrs
1000Hrs
15000Cycles

Only For SMD Series
All Series
Only For MOS
Except Oerlee 3

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